Introduction
  Baseline architecture

Proposed architecture
  Propsal # 1:
  Propsal # 2:

Experimental Analysis

Conclusion
Introduction

▶ High level bullet point goes here
  ▶ Second level bullet point here
▶ Second high level bullet point
A baseline decoupled architecture is shown below
Proposed Architectures
Speculative Parallelization in Decoupled Look-ahead
Weak Dependence Removal in Decoupled Look-ahead
Multi Column Example

- Here is how you do multi-column
Experimental Analysis

1.
Conclusion

- Decoupled look-ahead is capable of exploiting **implicit** parallelism
- Hardware support needed for decoupled look-ahead is **moderate**
Flexible Error Protection for Energy Efficient Reliable Architectures  
*T. Miller, N. Surapaneni, R. Teodorescu*, Ohio State Univ., SBAC-PAD’12

A Defect-Tolerant Accelerator for Emerging High Performance Applications  
*Olivier Temam*, INRIA France, ISCA’12

BenchNN: On the Broad Potential Applications Scope of Hardware NN Accelerators  
*T. Chen et.al.*, Univ. of Wisconsin, IISWC’12